

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

<b>INFORMATION</b> <b>DISCLOSURE</b> <b>CITATION</b> PTO-1449		<b>CUSTOMER</b> <b>NUMBER</b>  26615		ATTORNEY'S DKT No. H1493		APPLICATION No. 10/674,400	
				APPLICANT(S) Wiley Eugene HILL et al.			
				FILING DATE October 1, 2003		GROUP 2811	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes No	
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
Copy of U.S. Serial No. 10/728,844; filed December 8, 2003; entitled: "MERGED FINFET INVERTER/LOGIC GATE"; 51 pages.							
EXAMINER				DATE CONSIDERED			
[Signature]				05/20/04			